

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Kebichi *et al.*

Patent No. 7,036,064

Issued: April 25, 2006

Application No. 09/872,212

Filed: May 31, 2001

Confirmation No. 1993

For: SYNCHRONIZATION POINT ACROSS
DIFFERENT MEMORY BIST
CONTROLLERS

Examiner: Guy J. Lamarre

Art Unit: 2133

Attorney Reference No. 1011-58140-01

CERTIFICATE OF FILING

I hereby certify that this paper and the documents referred to as being attached or enclosed herewith are being filed via EFS on the date shown below.

Attorney or Agent

for Applicant(s) Justin D. Wagner/

Date Filed June 20, 2007

Filed via EFS June 20, 2007.

REQUEST FOR CERTIFICATE OF CORRECTION

The following printing errors were noted in comparing the printed patent with the papers in the attorneys' files:

In the Specification:

Column 8, line 21, "(n-11)" should be --(n-1)--.

Column 9, line 46, "Inn interleaved" should be --In interleaved--.

Column 10, line 12, "initialize and ran" should be --initialize and run--.

Column 10, line 47, "Inn the case" should be --In the case--.

Column 12, line 42, "ram 116x16" should be --ram 16x16--.

Column 12, line 46, "tool is then ran" should be --tool is then run--.

Column 16, line 42, "Inn this embodiment," should be --In this embodiment,--.

All of the above errors are attributable to the Patent Office, and a Certificate of Correction is enclosed to make formal notice of the errors in the referenced patent.

Respectfully submitted,

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cc: Docketing

**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**

PATENT NO. : 7,036,064

DATED : April 25, 2006

INVENTOR(S) : Kebichi et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 8, line 21, "(n-11) should be --(n-1)--.

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